

Notice of References Cited	Application/Control No. 09/608,743	Applicant(s)/Patent Under Reexamination JERREL P. HEIN, MARIUS GO	
	Examiner Walter F Briney III	Art Unit 2644	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Spanos, Costas J., Microelectronic Devices and Circuits, Fall 1999, University of California at Berkeley, Week 10 pages 1-8 http://www-inst.eecs.berkeley.edu/~ee105/fall99/lectures/lectw10.pdf
	V	Millman, Jacob Ph.D.; Halkias, Christos C. Ph.D.; Interated Electronics: Analog and Digital Circuits and Systems, 1972, McGraw-Hill, Inc. pages 126 and 254.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.